

ABSTRACT

The present invention provides a receiver apparatus for receiving digital data in which stuff data have been
5 inserted by stuffing synchronization. The receiver apparatus has a memory unit having a plurality of memory cells. The digital data other than the stuff data are written in the memory unit in synchronization with a write clock signal, and read from the memory cell in
10 synchronization with a read clock signal. The cycle of the read clock signal is adjusted on the basis of an interval from a reading address to an writing address of the memory cell.